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Verilog HDL: A Guide to Digital Design and Synthesis, 2nd Ed.

Verilog HDL: A Guide to Digital Design and Synthesis, Second Edition By Samir Palnitkar Publisher: Prentice Hall PTR Pub Date: February 21, 2003 ISBN: 0-13-044911-3 Pages: 496  
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ABSTRACT The Verilog-2001 Standard includes a number of enhancements that are targeted at simplifying designs, improving designs and reducing design errors. This paper details important enhancements that were added to the Verilog-2001 Standard that are intended to simplify behavioral modeling and to improve synthesis accuracy and efficiency.

Verilog-2001 Behavioral and Synthesis Enhancements

Jim Duckworth, WPI 3 Verilog Module Rev B Books • "FPGA Prototyping by Verilog Examples", 2008, Pong P. Chu, Wiley 978-0-470-18532-2 • "Starters Guide to Verilog 2001" by Ciletti, 2004, Prentice Hall 0-13-

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