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eas.caltech.edu/news/1174. Prof. P. P. Vaidyanathan has been with the Electrical Engineering department of the California Institute of Technology since 1983. His main research interests are in digital signal processing, sparse array signal processing, spectrum sensing and applications, multirate systems and filter banks, wavelet transforms, compressive sensing and sparse reconstruction, genomic signal processing, and signal processing for digital communications.

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Palghat P. Vaidyanathan is the Kiyoo and Eiko Tomiyasu Professor

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of Electrical Engineering at the California Institute of Technology, Pasadena, California, USA, where he teaches and leads research in the area of signal processing, especially digital signal processing, and its applications. He has authored four books, and authored or coauthored close to five hundred papers in various IEEE journals and conferences. Prof. Vaidyanathan received his B.Tech and M. Tech degrees from the Institute of Rad

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Publications - Dr Ravi Vaidyanathan

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The geometrical and algebraic view provided by Vaidyanathan is unique and probably deserves to be taught in any DSP course for engineers, even at the introductory level. Signal processing people however could find its style too intuitive but engineers should like it as a reference book in these topics.

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Multirate Systems And Filter Banks: P. P. Vaidyanathan ...

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Decimation, or down-sampling, reduces the sampling rate, whereas expansion, or up-sampling, followed by interpolation increases the sampling rate.

Multirate digital signal processing - Åbo Akademi

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in many areas of signal processing, such as filter bank theory

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