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Review DLD

*Lecture#1||chapter
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Q. 5.6: A sequential circuit with two D flip-flops A and B, two inputs, x and y; and one output z is

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~~6 Part 7: Examples~~

~~Chapter 7 Part 2:~~

~~Sequencer DLD~~

~~lecture #4 || Products~~

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~~siddiq Q. 5.1: The D~~

~~latch of Fig. 5.6 is~~

~~constructed with four~~

~~NAND gates and an~~

~~inverter. Consider the~~

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