

Cadence Orcad Pcb Designer Place And Route

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John Burkherth Jr is a career PCB Designer experienced in Military, Telecom, Consumer Hardware and lately, the Automotive industry. Originally, an RF specialist -- compelled to flip the bit now and then to fill the need for high-speed digital design. John enjoys playing bass and racing bikes when he's not writing about or performing PCB layout.

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Place and Route: This is the main design work that starts with a board outline and a netlist that pulls in the library and the logic. Floorplanning is the first step. Determining the ideal stack-up is a close second. More often than not, these attributes will go through some iterations along the way.

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Here we explore the design re-use place replicate functions in OrCAD Professional and above

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