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area, low power and minimal code footprint, enabling developers to achieve 32-bit performance at an 8-bit price point, bypassing the step to 16-bit devices.

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- ARM Debug Interface v5, Architecture Specification (ARM IHI 0031) Note A Cortex-M0 implementation can include a Debug Access Port (DAP). This DAP is defined in v5.1 of the ARM Debug interface specification, or in the errata document to Issue A of the ARM Debug Interface v5 Architecture Specification.
- Application Binary Interface for the ARM Architecture (The Base Standard) (IHI0036 ...

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~~Cortex-M0 Technical Reference Manual~~

Designed for smart and connected embedded applications, especially where size matters, the Cortex-M0 is the smallest Arm processor available, making it ideal for use in simple, cost-sensitive devices. Cortex-M0 is available to access for a low upfront cost with Arm DesignStart.

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The Cortex-M0+ processor has the smallest footprint and lowest power requirements of all the Cortex-M processors. The low-power processor is suitable for a wide variety of applications, including sensors and wearables. Features and Benefits. Add Intelligence. The Cortex-M0+ processor has the smallest footprint and lowest power requirements of all

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processor; Cortex-M System Design Kit (CMSDK) The Arm Cortex-M3 processor is the industry-leading 32-bit processor for highly ...

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- A simple architecture that is easy to learn and program.
- Ultra-low power, energy-efficient operation.
- Excellent code density.

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The ARM Cortex-M is a group of 32-bit RISC ARM processor cores licensed by Arm Holdings. These cores

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are optimized for low-cost and energy-efficient microcontrollers, which have been embedded in tens of billions of consumer devices. The cores consist of the Cortex-M0, Cortex-M0+, Cortex-M1, Cortex-M3, Cortex-M4, Cortex-M7, Cortex-M23, Cortex-M33, Cortex-M35P, Cortex-M55.

~~ARM Cortex M - Wikipedia~~

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